

FIG. 1

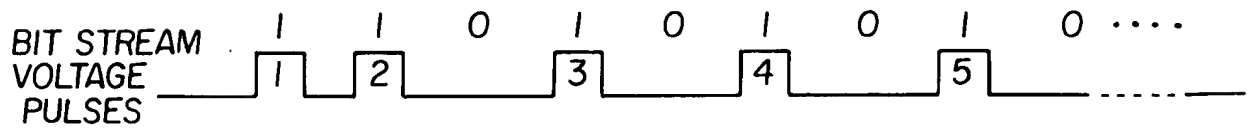


FIG. 2

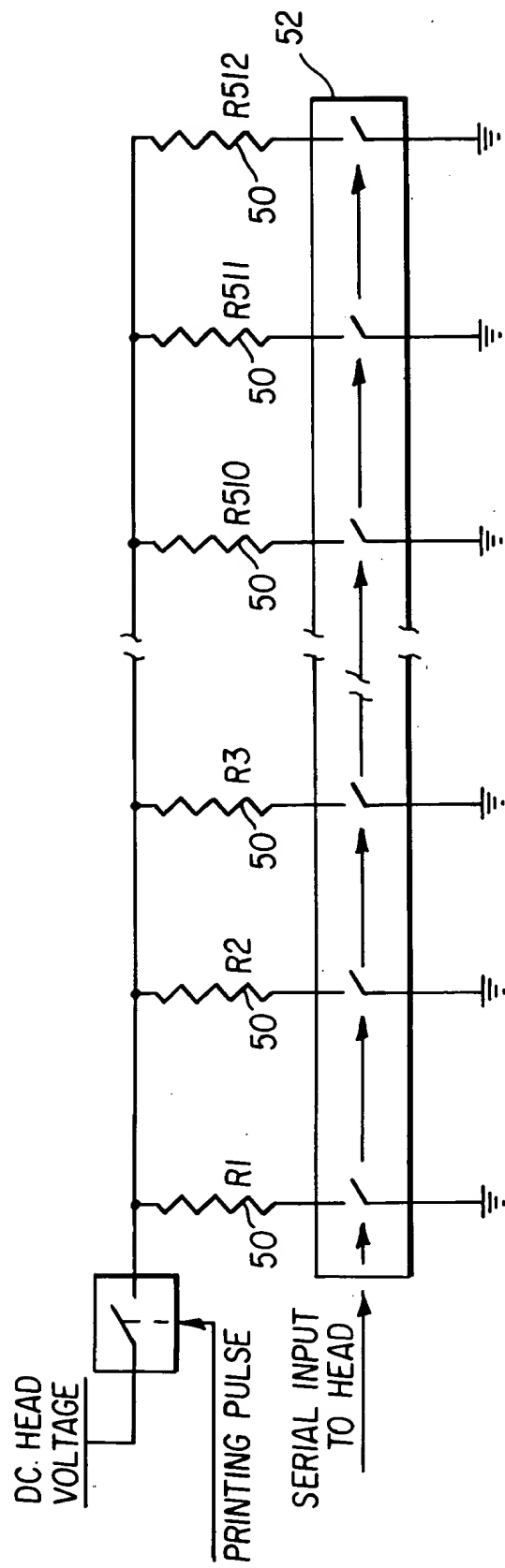


FIG. 3

Complement	63	62	61	60	59	58	57	...
Pixel Value	5	5	5	5	5	5	5	...
Sum	68	67	66	65	64	63	62	...
Sum(7 bit binary)	1000100	1000011	1000010	1000001	1000000	0111111	0111110	...
Modulation bit	1	1	1	1	1	0	0	...

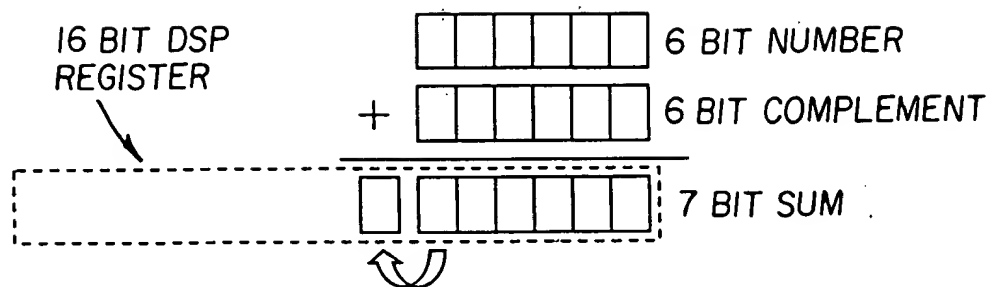
FIG. 4

Complement	63	58	61	57	62	60	59	...
Pixel Value	5	5	5	5	5	5	5	...
Sum	68	63	66	62	67	65	64	...
Sum(7 bit binary)	1000100	0111111	1000010	0111110	1000011	1000001	1000000	...
Modulation bit	1	0	1	0	1	1	1	...

FIG. 5

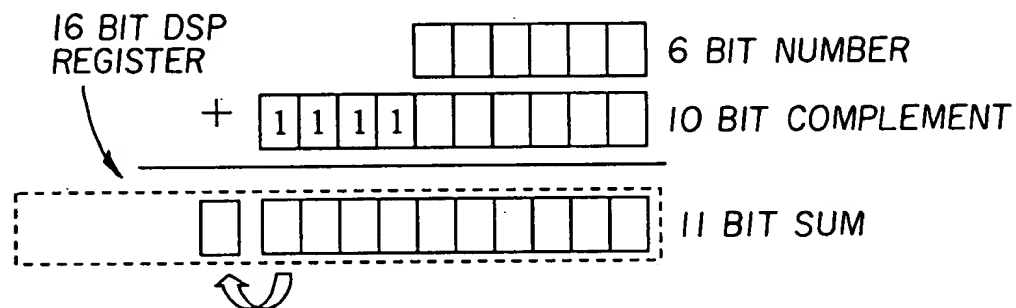
Complement	63	63	63	60	59	58	57	...
Pixel Value	5	5	5	5	5	5	5	...
Sum	68	68	68	65	64	63	62	...
Sum(7 bit binary)	1000100	1000100	1000100	1000001	1000000	0111111	0111110	...
Modulation bit	1	1	1	1	1	0	0	...

FIG. 6



THE OVERFLOW WILL GO INTO THE 7th BIT

FIG. 7



THE OVERFLOW WILL GO INTO THE 11th BIT

FIG. 8

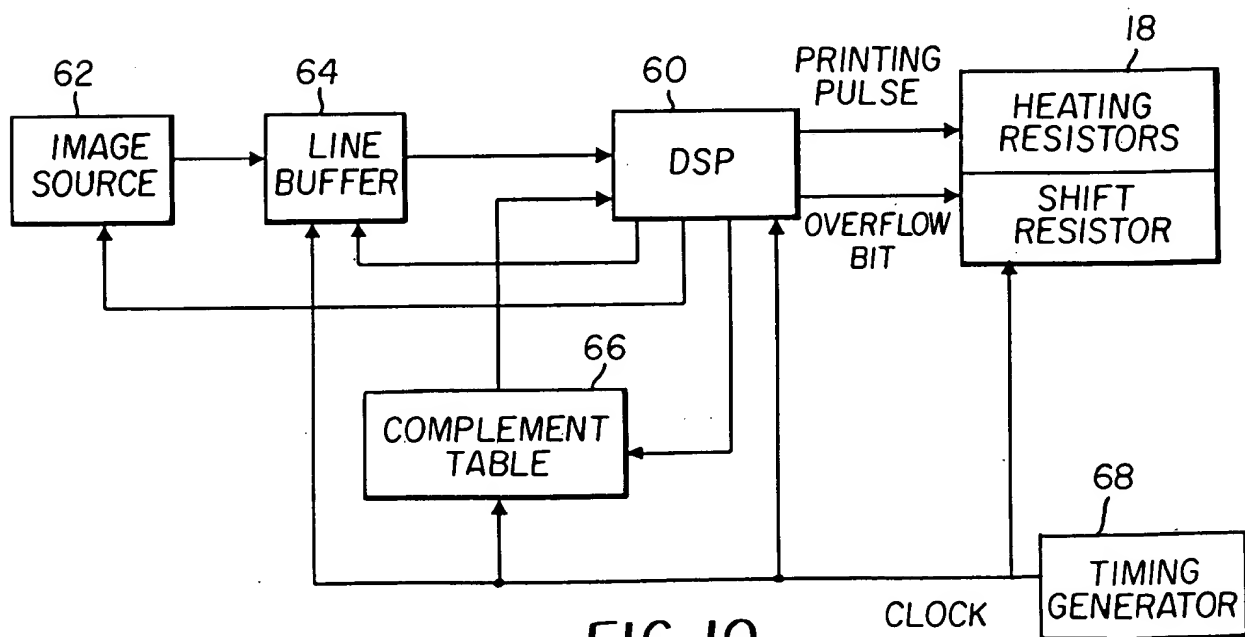


FIG. 10

